

ISQED 2012
Session Chairs

Day, Date	Session Title	Session Code	Session Chair	Session Chair E-mail	Session Co-chair	Session Co-chair E-mail
Tuesday, March 20, 2012						
	Test and Measurement	1A	Sreejit Chakravarty	Sreejit.Chakravarty@lsi.com	Srivatsa Vasudevan	srivats@synopsys.com
	Reliable System Design	1B	Payman Zarkesh-Ha	payman@ece.unm.edu	Srinivas Bodapati	srinivas.bodapati@intel.com
	System Frameworks and Tools	1C	Makram Mansour	makram.mansour@nsc.com	Jose Silva Matos	jsm@fe.up.pt
	Thermal and Power in 3D ICs	2A	Tan Yan	ytyantan@gmail.com	Martin Wong	mdfwong@ad.uiuc.edu
	Low Power Communication Circuits	2B	Syed Alam	smalam@gmail.com	Dinesh Somasekhar	Dinesh.Somasekhar@globalfoundries.com
	Process-Induced Variability & Hot Spot Detection	2C	Rajan Beera	Rajan.A.Beera@ti.com	Valeriy Sukharev	valeriy_sukharev@mentor.com
	Emerging Topics in EDA	3A	James Lei	lei_junzhao@yahoo.com	Anand Iyer	anand.iyer@amd.com
	Design & Analysis of Emerging Devices	3B	Paul Tong	PTong@PERICOM.com	Bao Liu	bliu@utsa.edu
	Variation-Aware Design Methodologies	3C	Rajesh Garg	rajesh.garg@intel.com	Riaz Naseer	riaz.naseer@intel.com
	Poster Papers		Mark Budnik	mark.budnik@valpo.edu	Keith Bowman	keith.a.bowman@intel.com
Wednesday, March 21, 2012						
	Physical Design	4A	Makoto Ikeda	ikeda@silicon.u-tokyo.ac.jp	Martin Wong	mdfwong@ad.uiuc.edu
	Robust SRAM Design	4B	Srinivas Bodapati	srinivas.bodapati@intel.com	Hamid Mahmoodi	mahmoodi@sfsu.edu
	3D Effects on Package Co-Design	4C	Kamesh Gadepally	Kamesh.Gadepally@ti.com	Lalitha Immaneni	lalitha.immaneni@intel.com
	Advanced Analysis & Characterization for Sub-Micron Design	5A	Masahiro Fujita	fujita@ee.t.u-tokyo.ac.jp	Anand Iyer	anand.iyer@amd.com
	Power-Aware Design	5B	Cheng Zhuo	cheng.zhuo@intel.com	Mark Budnik	mark.budnik@valpo.edu
	Circuit-Level Variability & Manufacturability	5C	Shubhankar Basu	Shubhankar.Basu@globalfoundries.com	Saraju Mohanty	Saraju.Mohanty@unt.edu
	Verification & Silicon Debug	6A	Srivatsa Vasudevan	srivats@synopsys.com	Sreejit Chakravarty	Sreejit.Chakravarty@lsi.com
	Challenges & Oppotunities in New Technologies	6B	Paul Tong	PTong@PERICOM.com	Bao Liu	bliu@utsa.edu
	Energy-Aware System Design	6C	Lech Jozwiak	L.Jozwiak@tue.nl	Rajesh Berigei	Rajesh.R.Berigei@nsc.com